# COMP3500: Project 5 Part 4 Implementing LRU in TLB

**Exercise 1:** Work with your group member to extend the data structure of TLB, where the LRU replacement policy will be implemented:

**Exercise 2:** Consider a single-level paging scheme with no data cache. The TLB has 32 entries. The TLB access time is 10 ns; memory access time is 200ns.